CAPABILITY STATEMENT

ASIC Engineering Inc. – DBA SlimPixel

CAGE:**14C22** | UIE: **GEYCU1TUGXL7** | DUNS: **029209686**

INTRODUCTION

1. Ultra-low Size, Weight, Power and Cost (SWaP-C) digital video encoding for transmission or storage. Only 1/20th of NVIDIA's power for similar frame resolution and speed.
2. Extreme frame-rate (1KFPS) video encoding system.

CORE COMPETENCIES

1. Silicon Integrated Circuit technology using Massively Parallel and Distributed Memory (MPDM).
2. Optimize **drone** flight length/duration. Increase payload size. Enhance fighting capabilities.
3. Dramatically reduce AI Data Center operation cost by coding video with MPDM.
4. Enable interception of **hypersonic** missile threats with extreme frame-rate video coding.

PAST PERFORMANCE

Proven prototype fabricated by TSMC in 40nm silicon CMOS. Ultra-low power measured.

DIFFERENTIATORS

No Central Processing Unit (CPU) nor Graphics Processing Unit (GPU). No software. Dedicate data-driven engine. No waste of energy. 20 times less power than leading manufacturer. Being reworked for 14nm CMOS.

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